

**AMENDMENTS TO THE SPECIFICATION**

Please add paragraph [0014a] after paragraph [0014] as follows:

[0014a] FIG. 8 is a schematic diagram showing pixel sections and decoder sections of a pixel array.

Please add paragraph [0027a] after paragraph [0027] as follows:

[0027a] FIG. 8 is a schematic diagram showing pixel sections 701 and decoder sections 702 of an example pixel array. The pixel sections 701 have an associated routing line 703 having a first pitch. The decoder sections 702 have an associated routing line 602 having a second pitch. Although it may not be apparent, the first pitch is larger than the second pitch.